IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Han-Gu Sohn et al.

Serial No.: 10/732,868

Filed: December 10, 2003

Examiner: Dare, Ryan A.

Group: Art Unit 2186

Docket: 8729-226 (SS-20062-US)

For: **CIRCUITS AND METHODS FOR PROVIDING VARIABLE DATA I/O WIDTH FOR SEMICONDUCTOR MEMORY DEVICES**

Commissioner of Patents
P.O. Box 1450
Alexandria, VA. 22313-1450

Amendment

This is a response to the Office Action mailed on October 6, 2006. Please amend the claims as follows: